

**REMARKS**

At the outset, Applicants thank the Examiner for the thorough review and consideration of the subject application. The Final Office Action of August 26, 2003 has been received and contents carefully reviewed.

In the Final Office Action, the Examiner objected to claim 14 due to grammatical informalities and suggested language that would make claim 14 allowable; rejected claims 1-12, 22, and 23 under 35 U.S.C. § 103(a) as being unpatentable over Gu et al. (U.S. Pat. No. 6,359,672); and rejected claims 13 and 15-21 under as being unpatentable under 35 U.S.C. § 103(a) as being unpatentable over Gu et al. in view of Yamazaki (U.S. Pat. No. 5,463,483). The rejections and objections of these claims is traversed and reconsideration of the claims is respectfully requested in view of the following remarks.

By this Amendment, Applicants hereby cancel claim 14, amend claims 1, 7, 12, 13, and 22, add new claims 24-60, and respectfully submit no new matter has been entered. Accordingly, claims 1-13 and 15-60 are proposed to be pending in this application.

Applicants thank the Examiner for the indication of allowable subject matter in claim 14. Applicants have amended claims 13 and 22 to include the features of claim 14 consistent with the Examiner's suggestion in the Final Office Action. Accordingly, Applicants believe that claims 13 and 22 and claims 15-21, 23, 33-42, and 44-60, which variously depend from claims 13 and 22, are in condition for immediate allowance.

The rejection of claims 1-12, 22, and 23 under 35 U.S.C. § 103(a) as being unpatentable over Gu et al. is traversed and reconsideration is respectfully requested.

Independent claim 1 is allowable over Gu et al. in that claim 1 recites a combination of elements including, for example, "forming the pixel electrode on the organic insulating

film so as to be overlapped, by a predetermined area, with at least one of the gate line and the data line, to optimally prevent light leakage between the pixel electrode and the at least one of the gate line and the data line, wherein an optimal prevention of light leakage uses a minimal overlapping area for the predetermined area.” Gu et al. fails to teach or suggest at least this feature of the claimed invention. Accordingly, Applicant respectfully submits that claims 2-6, which depend from claim 1, are also allowable over Gu et al.

Independent claim 7 is allowable over Gu et al. in that claim 7 recites a combination of elements including, for example, “a pixel electrode formed on the organic insulating film and connected to a source electrode of the thin film transistor, said pixel electrode overlapping the gate line and the data line, wherein a parasitic capacitance in an overlapping area between the pixel electrode and the data line is different from a parasitic capacitance in an overlapping area between the pixel electrode and the gate line.” Gu et al. fails to teach or suggest at least this feature of the claimed invention. Accordingly, Applicant respectfully submits that claims 8-12, which depend from claim 7, are also allowable over Gu et al.

Independent claim 22 is allowable over Gu et al. in that claim 22 recites a combination of elements including, for example, “wherein the thickness and the dielectric constant of the organic insulating film are selected such that a liquid crystal pixel cell driven with the pixel electrode charges to a voltage which is more than 95% of a video data voltage within  $\frac{1}{2}$  of an enabling interval of a control signal that is applied to a gate electrode for defining a channel of the thin film transistor.” Gu et al. fails to teach or suggest at least this feature of the claimed invention. Accordingly, Applicant respectfully submits that claim 23, which depends from claim 22, is also allowable over Gu et al.

The rejection of claims 13 and 15-21 under 35 U.S.C. § 103(a) as being unpatentable over Gu et al. in view of Yamazaki is traversed and reconsideration is respectfully requested.

Independent claim 13 is allowable over Gu et al. in view of Yamazaki in that claim 13 recites a combination of elements including, for example, “a pixel electrode... overlapped with the gate line and the data line with having an organic insulating film therebetween, wherein a thickness and a dielectric constant of the organic insulating film are selected such that a signal delay is less than 10  $\mu$ sec for each of the gate lines and the data line, wherein the thickness and the dielectric constant of the organic insulating film are selected such that a liquid crystal pixel cell driven with the pixel electrode charges to a voltage which is more than 95% of a video data voltage within  $\frac{1}{2}$  of an enabling interval of a control signal that is applied to a gate electrode for defining a channel of the thin film transistor.” Neither Gu et al. nor Yamazaki, singly or in combination, teaches or suggests at least this feature of the claimed invention. Accordingly, Applicant respectfully submits that claims 15-21, which depend from claim 13, are also allowable over Gu et al. in view of Yamazaki.

If the Examiner deems that a telephone conversation would further the prosecution of this application, the Examiner is invited to call the undersigned at (202) 496-7500.

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If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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